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PARK et al.(10) **Pub. No.: US 2018/0219053 A1**(43) **Pub. Date: Aug. 2, 2018**(54) **ORGANIC LIGHT EMITTING DIODE
DISPLAY DEVICE***H01L 27/12* (2006.01)*H01L 51/50* (2006.01)(71) Applicant: **LG Display Co., Ltd.**, Seoul (KR)(52) **U.S. Cl.**(72) Inventors: **Hong-Ki PARK**, Goyang-si (KR);
Hyo-Dae BAE, Paju-si (KR)CPC **H01L 27/3246** (2013.01); **H01L 51/56**
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27/3262 (2013.01); **H01L 27/1214** (2013.01);
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(57)

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Disclosed is an organic light emitting diode (OLED) display device including a substrate including a pixel region and a boundary region outside the pixel region. The pixel region comprises an area having a short side and a long side. The pixel region comprises an array of pixels to emit light. The OLED display device includes a substrate in the pixel region and in the boundary region. The OLED display device further includes a first electrode of a light emitting device in the pixel region over the substrate, a first bank covering edges of the first electrode in the pixel region on the substrate in the boundary region, wherein a width of an edge of the first bank along the short side of the pixel region is different from a width of an edge of the first bank along the long side of the pixel region, and a second bank on a portion of the first bank in the boundary region.

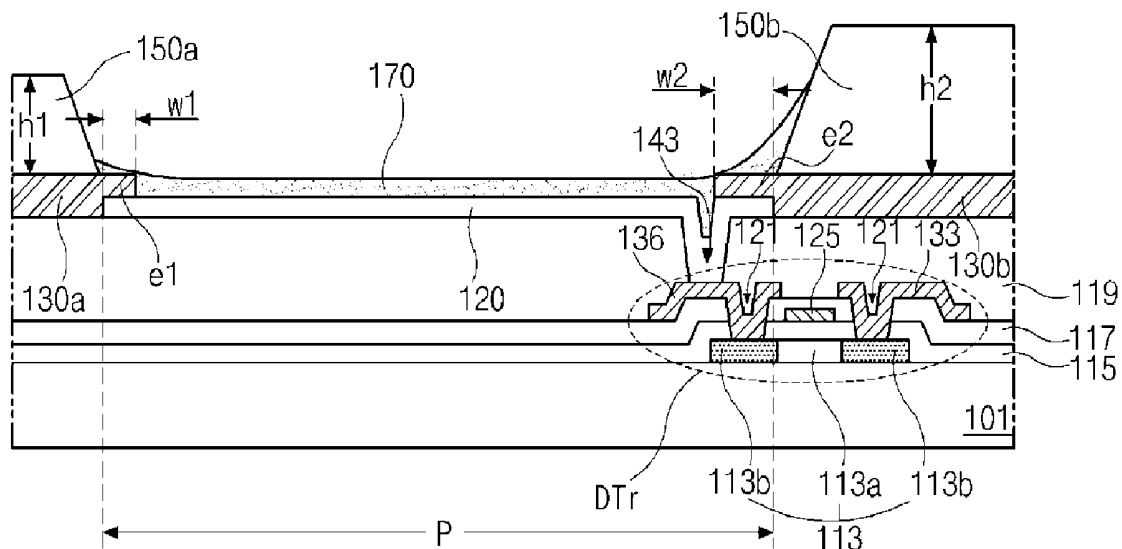


FIG. 1
RELATED ART

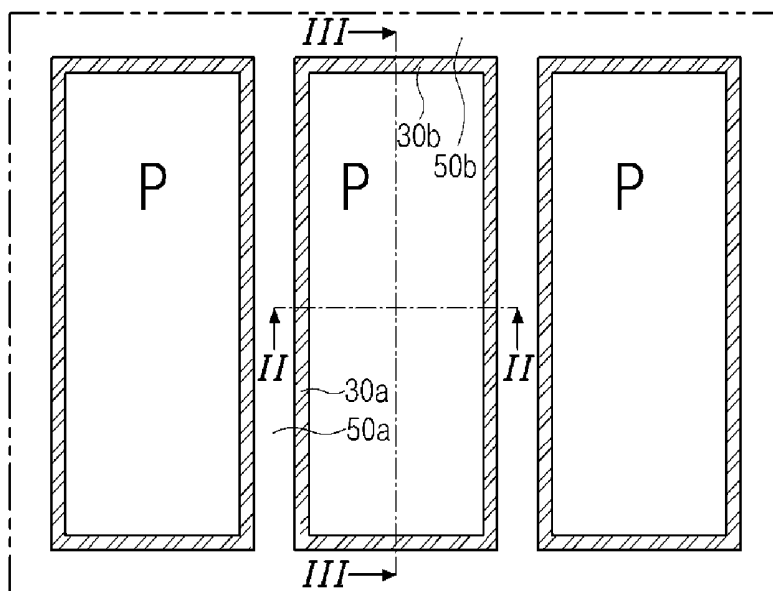


FIG. 2
RELATED ART

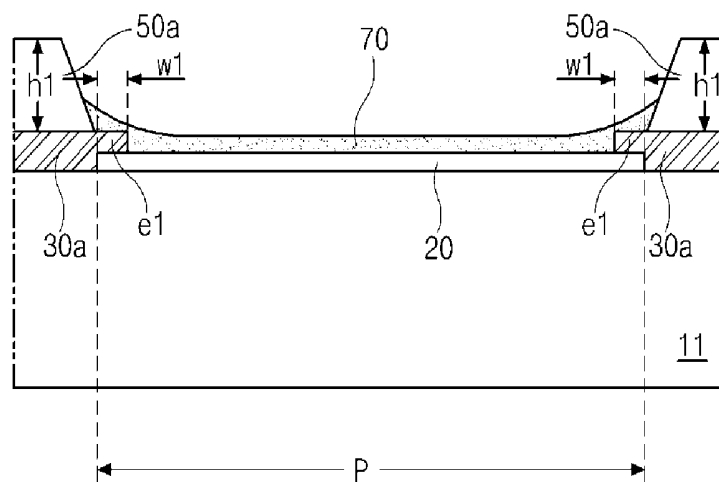


FIG. 3
RELATED ART

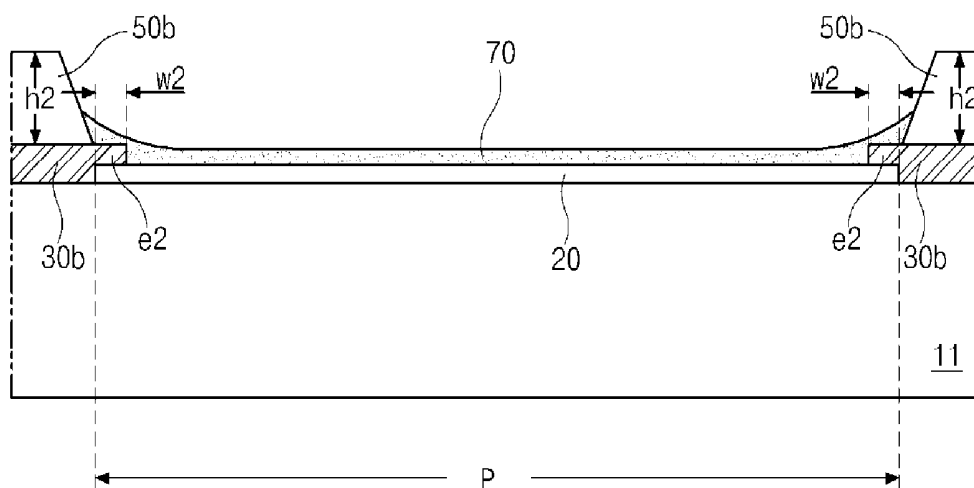


FIG. 4

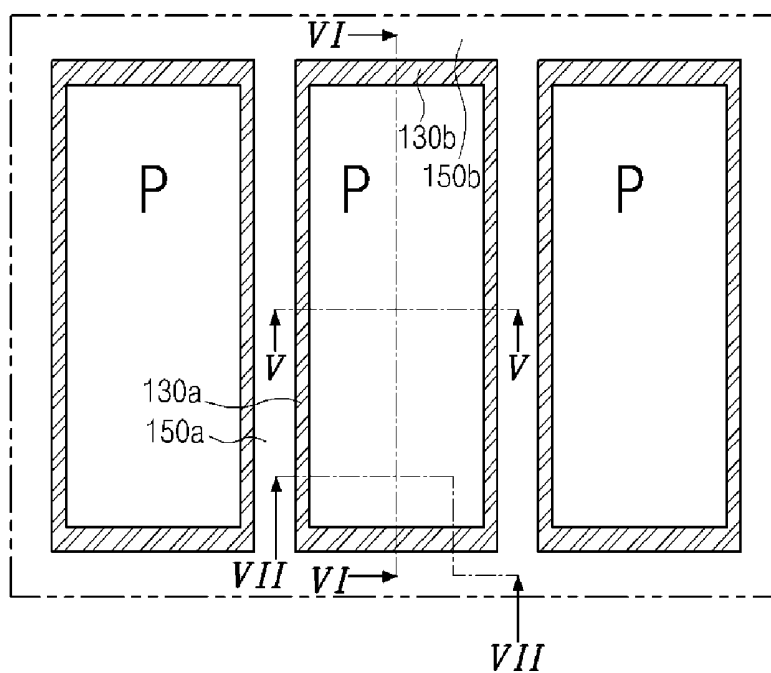


FIG. 5

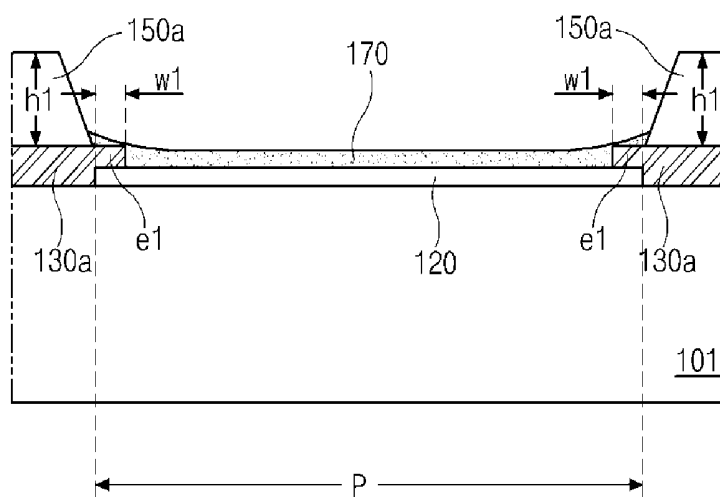
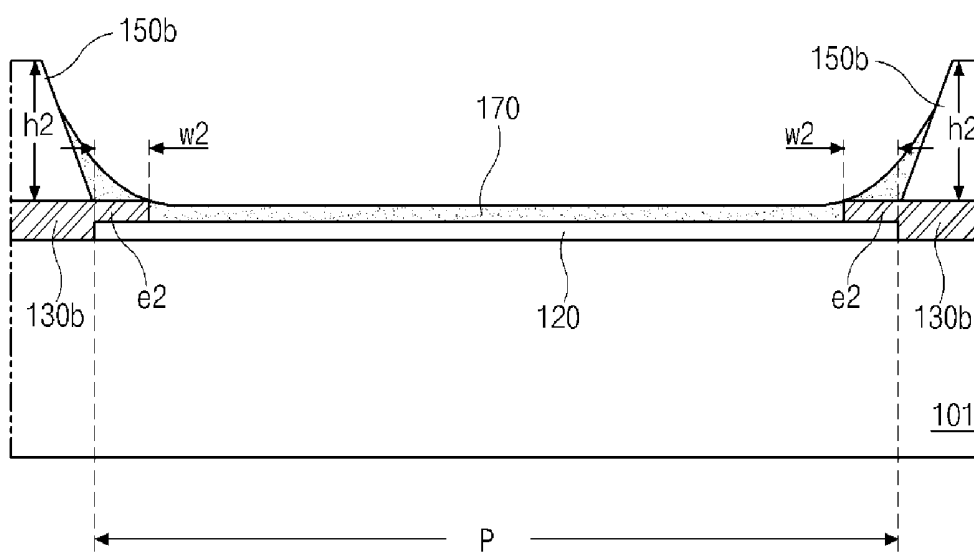
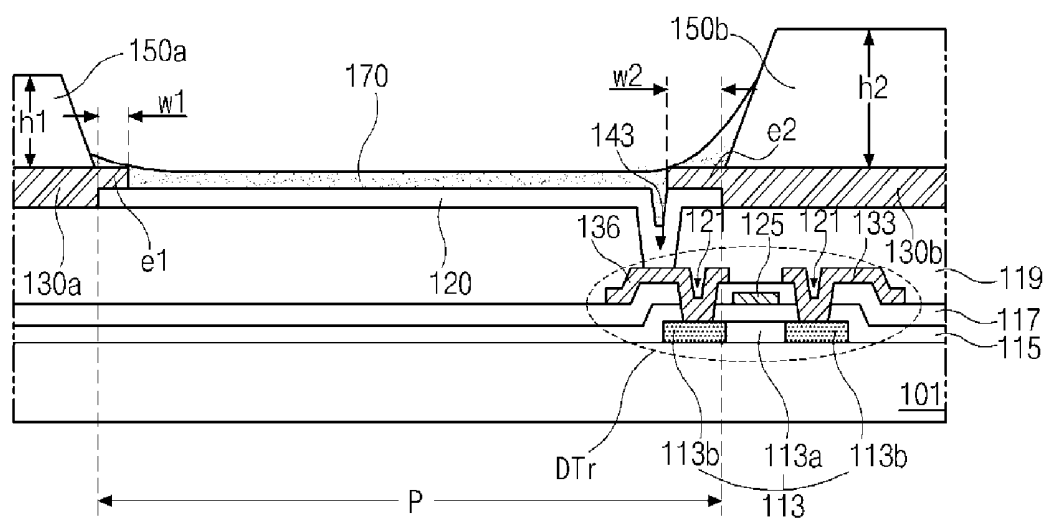


FIG. 6





ORGANIC LIGHT EMITTING DIODE DISPLAY DEVICE

RELATED APPLICATIONS

[0001] The present application is a divisional application of U.S. patent application Ser. No. 15/183,727 filed on Jun. 15, 2016, which claims the priority benefit of Korean Patent Application No. 10-2015-0093651 filed in Republic of Korea on Jun. 30, 2015, each of which are hereby incorporated by reference in their entirety for all purposes as if fully set forth herein.

BACKGROUND OF THE INVENTION

Field of the Invention

[0002] The present invention relates to an organic light emitting diode (OLED) display device. In particular, the present invention relates to an OLED display device that has improved uniformity of thickness of an organic light emitting layer.

Discussion of the Related Art

[0003] Recently, flat display devices, such as a plasma display panel (PDP), a liquid crystal display (LCD) device, and an organic light emitting diode (OLED) display device, have been researched.

[0004] Among the flat display devices, the OLED display device is a self-luminescent device and can have a thin profile because the OLED display device does not need a backlight used for the LCD device.

[0005] Further, compared with the LCD device, the OLED display device has advantages of excellent viewing angle and contrast ratio, low power consumption, operation in low DC voltage, fast response speed, being strong to an external impact because of its solid internal components, and wide operating temperature range.

[0006] Particularly, since processes of manufacturing the OLED display device are simple, production cost of the OLED display device can be reduced more than that of the LCD device.

[0007] FIG. 1 is a plan view illustrating an OLED display device according to the related art.

[0008] As shown in FIG. 1, the related art OLED display device includes a plurality of pixel regions P, a first bank 30a and 30b separating the pixel regions P and located at a boundary portion of each pixel region P, and a second bank 50a and 50b exposing edges of the first bank 30a and 30b and located on the first bank 30a and 30b.

[0009] The first bank 30a and 30b is divided into a first portion 30a along a long side of the pixel region P and a second portion 30b along a short side of the pixel region P, and the second bank 50a and 50b is divided into a third portion 50a along the long side of the pixel region P and a fourth portion 50b along the short side of the pixel region P.

[0010] A width of the edge of the first portion 30a is equal to that of the second portion 30b.

[0011] FIG. 2 is a cross-sectional view taken along a line II-II of FIG. 1, and FIG. 3 is a cross-sectional view taken along a line of FIG. 1. In more detail, FIG. 2 is a cross-sectional view of the pixel region P and the boundary portion thereof along a minor axis of the pixel region, and FIG. 3 is a cross-sectional view of the pixel region P and the boundary portion thereof along a major axis of the pixel region.

[0012] As shown in FIGS. 2 and 3, the related art OLED display device includes a first electrode 20 in the pixel region P on a substrate 11, the first bank 30a and 30b at the boundary portion of the pixel region P on the substrate 11, the second bank 50a and 50b on the first bank 30a and 30b, and an organic light emitting layer 70 on the first electrode 20.

[0013] The first bank 30a and 30b is arranged to cover edges of the first electrode 20, and the second bank 50a and 50b is arranged to expose the edges of the first bank 30a and 30b.

[0014] A width w1 of an edge e1 of the first portion 30a is equal to a width w2 of an edge e2 of the second portion 30b.

[0015] For example, the width w1 of the edge e1 of the first portion 30a and the width w2 of the edge e2 of the second portion 30b may be equally 1 μ m to 10 μ m.

[0016] Further, a height h1 of the third portion 50a is equal to a height h2 of the fourth portion 50b.

[0017] For example, the height h1 of the third portion 50a and the height h2 of the fourth portion 50b may be equally 1 μ m to 10 μ m.

[0018] The first electrode 20 may be made of a transparent conductive material having a relatively greater work function to function as an anode. The first bank 30a and 30b may be made of an inorganic material having a hydrophilicity, and the second bank 50a and 50b may be made of an organic material having a hydrophobicity.

[0019] The OLED display device further includes a driving thin film transistor DTr located between the substrate 11 and the first electrode 20 and connected to the first electrode 20.

[0020] The organic light emitting layer 70 is laminated using a soluble process such as an inkjet printing method, a nozzle printing method or the like.

[0021] In detail, the organic light emitting layer 70 is formed by dropping an organic light emitting material solution on the first electrode 20 and then drying the organic light emitting material solution.

[0022] A phenomenon occurs that a thickness of an edge portion of the organic light emitting layer 70 is greater than a thickness of a center portion of the organic light emitting layer 70 in the drying process. This phenomenon is referred to as a pile-up phenomenon.

[0023] In this case, since the edges e1 and e2 of the first bank 30a and 30b is exposed by the second bank 50a and 50b, the dropped organic light emitting material solution contacts top surfaces of the edges e1 and e2 of the first bank 30a and 30b.

[0024] Further, since the first bank 30a and 30b is made of an hydrophilic inorganic material, the first bank 30a and 30b has a surface energy greater than that of the second bank 50a and 50b, and a surface tension of the organic light emitting material solution at the top surfaces of the edges e1 and e2 of the first bank 30a and 30b is reduced.

[0025] Accordingly, the pile-up phenomenon is induced from the top surfaces of the edges e1 and e2 of the first bank 30a and 30b to side walls of the second bank 50a and 50b.

[0026] Thus, the pile-up phenomenon at the boundary portion of a display region out of the pixel region P can be relieved to some extent.

[0027] In other words, a uniformity of thickness of the display region of the pixel region P can be improved to some extent.

[0028] However, since the pile-up phenomenon at the boundary portion is not completely improved, a nonuniformity of brightness still occurs due to reduction of a uniformity of thickness of the organic light emitting layer **70** and light emission efficiency and lifetime of the OLED display device are reduced.

[0029] Further, since the dual banks of the first bank **30a** and **30b** and the second bank **50a** and **50b** are arranged vertically, an aperture ratio of the OLED display device is reduced compared with the LCD device.

SUMMARY OF THE INVENTION

[0030] Accordingly, the present invention is directed to an OLED display device that substantially obviates one or more of the problems due to limitations and disadvantages of the related art.

[0031] An object of the present invention is to improve a uniformity of thickness of an organic light emitting layer and a nonuniformity of brightness.

[0032] Additional features and advantages of the disclosure will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the disclosure. The advantages of the disclosure will be realized and attained by the structure particularly pointed out in the written description and claims as well as the appended drawings.

[0033] To achieve these and other advantages, and in accordance with the purpose of the present invention, as embodied and broadly described herein, an organic light emitting diode (OLED) display device includes pixel region and a boundary region outside the pixel region. The pixel region comprises an area having a short side and a long side. The pixel region comprises an array of pixels to emit light. The OLED display device includes a substrate in the pixel region and in the boundary region, a first electrode of a light emitting device in the pixel region over the substrate, a first bank covering edges of the first electrode in the pixel region on the substrate in the boundary region, and a second bank on a portion of the first bank in the boundary region, wherein a width of an edge of the first bank along the short side of the pixel region is different from a width of an edge of the first bank along the long side of the pixel region.

[0034] In another embodiment, an organic light emitting display (OLED) device includes a pixel region and a boundary region outside the pixel region. The pixel region comprises an area having a short dimension and a long dimension. The pixel region comprises an array of pixels to emit light. A substrate is in the pixel region and in the boundary region. An electrode is on the substrate in the pixel region. The electrode has long edge portions oriented along the long dimension of the pixel region and the electrode has short edge portions oriented along the short dimension of the pixel region. A first bank is on the substrate in the boundary region and on the long and the short edge portions of the electrode in the pixel region. A second bank is on the first bank in the boundary region. The second bank has long edge portions oriented along the long dimension of the pixel region and short edge portions oriented along the short dimension of the pixel region. An organic light emitting layer is on the electrode and on the first bank in the pixel region and on at least a portion of the second bank in the boundary region. An average height of short edge portions of the organic light

emitting layer on the second bank is greater than an average height of long edge portions of the organic light emitting layer on the second bank.

[0035] In another embodiment, a display device comprises a plurality of pixel regions each comprising an area having a short dimension and a long dimension. Each of the pixel regions comprises an array of pixels to emit light. A plurality of boundary regions separates the pixel regions. The boundary regions comprise a first bank and a second bank. The first comprises a hydrophilic material around a perimeter of the pixel region. A second bank comprising a hydrophobic material on a portion of the first bank around the perimeter of the pixel region. The second bank has long edge portions along the long dimension and short edge portions along the short dimension. A first height of the short edge portions of the second bank is greater than a second height of the long edge portions of the second bank.

[0036] It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

[0037] The accompanying drawings, which are included to provide a further understanding of the disclosure and are incorporated in and constitute a part of this specification, illustrate embodiments of the disclosure and together with the description serve to explain the principles of the disclosure. In the drawings:

[0038] FIG. 1 is a plan view illustrating an OLED display device according to the related art;

[0039] FIG. 2 is a cross-sectional view taken along a line II-II of FIG. 1;

[0040] FIG. 3 is a cross-sectional view taken along a line of FIG. 1;

[0041] FIG. 4 is a plan view illustrating an OLED display device according to an embodiment of the present invention;

[0042] FIG. 5 is a cross-sectional view taken along a line V-V of FIG. 4;

[0043] FIG. 6 is a cross-sectional view taken along a line VI-VI of FIG. 4; and

[0044] FIG. 7 is a cross-sectional view taken along a line VII-VII of FIG. 4.

DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

[0045] Reference will now be made in detail to embodiments, examples of which are illustrated in the accompanying drawings. The same or like reference numbers may be used throughout the drawings to refer to the same or like parts.

[0046] FIG. 4 is a plan view illustrating an OLED display device according to an embodiment of the present invention.

[0047] As shown in FIG. 4, the OLED display device of the embodiment includes a plurality of pixel regions P, a first bank **130a** and **130b** in a boundary region separating the pixel regions P and located at a boundary portion of each pixel region P, and a second bank **150a** and **150b** in the boundary region not covering (e.g., exposing) edges of the first bank **130a** and **130b** and located on the first bank **130a** and **130b**. The edges of the first bank **130a** and **130b** not covered by the second bank **150a** and **150b** may be covered by a different layer, such as organic light emitting layer as

described below. The first bank **130a** and **130b** and second bank **150a** and **150b** may be located around a perimeter of the pixel region P.

[0048] The first bank **130a** and **130b** is divided into a first portion **130a** (e.g., a long edge portion) along a long side of the pixel region P and a second portion **130b** (e.g., a short edge portion) along a short side of the pixel region P, and the second bank **150a** and **150b** is divided into a third portion **150a** (e.g., a long edge portion) along the long side of the pixel region P and a fourth portion **150b** (e.g., a short edge portion) along the short side of the pixel region P.

[0049] A width of the edge of the second portion **130b** is greater than that of the first portion **130a**.

[0050] FIG. 5 is a cross-sectional view taken along a line V-V of FIG. 4, FIG. 6 is a cross-sectional view taken along a line VI-VI of FIG. 4, and FIG. 7 is a cross-sectional view taken along a line VII-VII of FIG. 4. In more detail, FIG. 5 is a cross-sectional view of the pixel region P and the boundary portion thereof along a minor axis of the pixel region, FIG. 6 is a cross-sectional view of the pixel region P and the boundary portion thereof along a major axis of the pixel region, and FIG. 7 is a cross-sectional view of the pixel region P and the boundary portion thereof with a driving thin film transistor.

[0051] As shown in FIGS. 5-7, the OLED display device of the embodiment includes a first electrode **120** on or over the substrate **101** in the pixel region P and in a boundary region separating the pixel regions P, the first bank **130a** and **130b** covering edges of the first electrode **120** in the pixel region P and on the substrate **101** in a boundary region surrounding the pixel region P, the second bank **150a** and **150b** on a portion of the first bank **130a** and **130b** in the boundary region, and an organic light emitting layer **170** on the first electrode **120** and on the first bank **130a** and **130b** in the pixel region and on at least a portion of the second bank in the boundary region.

[0052] The first bank **130a** and **130b** is arranged to cover edges of the first electrode **120**, and the second bank **150a** and **150b** is arranged to not cover the edges **e1** and **e2** of the first bank **130a** and **130b** (which may be covered by a different layer such as the organic light emitting layer **170**).

[0053] A width **w1** of the edge **e1** of the first portion **130a** is different from a width **w2** of the edge **e2** of the second portion **130b**.

[0054] In detail, the width **w2** of the edge **e2** of the second portion **130b** is greater than the width **w1** of the edge **e1** of the first portion **130a**.

[0055] For example, the width **w2** of the edge **e2** of the second portion **130b** may be about 7.5 μm to about 10 μm , and the width **w1** of the edge **e1** of the first portion **130b** may be about 5 μm . In other example, the width **w2** of the edge **e2** of the second portion **130b** may be about 6 μm to about 8 μm , and the width **w1** of the edge **e1** of the first portion **130b** may be about 4 μm . In other example, the width **w2** of the edge **e2** of the second portion **130b** may be about 4.5 μm to about 6 μm , and the width **w1** of the edge **e1** of the first portion **130b** may be about 3 μm . Thus, in one embodiment, the width **w2** may be in the range of 1.5-2 times the width **w1**.

[0056] Further, a height **h1** of the third portion **150a** is different from a height **h2** of the fourth portion **150b**.

[0057] In detail, the height **h2** of the fourth portion **150b** is greater than the height **h1** of the third portion **150a**.

[0058] For example, the height **h2** of the fourth portion **150b** may be about 1.5 μm to 2 μm , and the height **h1** of the third portion **150a** may be about 1 μm . In other example, the height **h2** of the fourth portion **150b** may be about 2.5 μm to 3 μm , and the height **h1** of the third portion **150a** may be about 2 μm . Thus, for example, the height **h2** may be in the range of 1.25-2 times the height **h1**. Further, in an embodiment, the height **h2** of the fourth portion **150b** may be higher than the height **h1** of the third portion **150a** at least by 0.5 μm , and the height of **h2** of the fourth portion **150b** may be 3 μm or less.

[0059] In this case, using a half-tone mask or the like including a blocking region, a semi-transmissive region and a transmissive region, the heights **h1** and **h2** of the second bank **150a** and **150b** may be formed differently.

[0060] Accordingly, the second bank **150a** and **150b** having the different heights **h1** and **h2** can be formed, and thus production process can be simplified.

[0061] The first electrode **120** may be made of a transparent conductive material, for example, indium-tin-oxide (ITO) or indium-zinc-oxide (IZO), having a relatively greater work function to function as an anode.

[0062] The first bank **130a** and **130b** may be made of an inorganic material, for example, silicon oxide (SiO_2) or silicon nitride (SiN_x), having a hydrophilicity. The second bank **150a** and **150b** may be made of an organic material, for example, polyacryl, polyimide, polyamide (PA), benzocyclobutene (BCB), or phenol resin, having a hydrophobicity.

[0063] The OLED display device further includes the driving thin film transistor DTr located between the substrate **101** and the first electrode **120** and connected to the first electrode **120**.

[0064] The detailed structure of the driving thin film transistor DTr is explained with reference to FIG. 7.

[0065] A semiconductor layer **113** is located on the substrate **101** and includes a first region **113a** made of intrinsic polysilicon, and a second region **113b** located at each of both sides of the first region **113a** and doped with highly concentrated impurities.

[0066] A gate insulating layer **115** is located entirely on the substrate **101** and covers the semiconductor layer **113**. A gate electrode **125** is located on the gate insulating layer **115** corresponding to the first region **113a**. An inter-layered insulating layer **117** covers the gate electrode **125** and is entirely on the substrate **101**.

[0067] A semiconductor contact hole **121** exposing each second region **113b** is formed in the gate insulating layer **115** and the inter-layered insulating layer **117**.

[0068] Source and drain electrodes **133** and **136** are located on the inter-layered insulating layer **117**, are connected to the respective second regions **113b** through the respective semiconductor contact holes **121**, and are spaced apart from each other.

[0069] The source and drain electrodes **133** and **136**, the semiconductor layer **113**, the gate electrode **125** form the driving thin film transistor DTr.

[0070] A passivation layer **119** is located on the driving thin film transistor DTr over the entire substrate **101**, and has a planarization property.

[0071] A drain contact hole **143** exposing the drain electrode **136** is formed in the passivation layer **119**. The drain electrode **136** is connected to the first electrode **120** through the drain contact hole **143**.

[0072] The organic light emitting layer 170 is located on the first electrode 120. A second electrode (not shown) is located on the organic light emitting layer 170 and the second bank 150a and 150b.

[0073] The organic light emitting layer 170 is laminated using a soluble process such as an inkjet printing method, a nozzle printing method or the like.

[0074] The organic light emitting layer 170 is formed by dropping an organic light emitting material solution on the first electrode 120 and then drying the organic light emitting material solution.

[0075] A phenomenon occurs that a thickness of an edge portion of the organic light emitting layer 170 is greater than a thickness of a center portion of the organic light emitting layer 170 in the drying process. This phenomenon is referred to as a pile-up phenomenon.

[0076] In this case, since the edges e1 and e2 of the first bank 130a and 130b are exposed by the second bank 150a and 150b, the dropped organic light emitting material solution contacts top surfaces of the edges e1 and e2 of the first bank 130a and 130b.

[0077] Further, since the first bank 130a and 130b is made of an hydrophilic inorganic material, the first bank 130a and 130b has a surface energy greater than that of the second bank 150a and 150b, and a surface tension of the organic light emitting material solution at the top surfaces of the edges e1 and e2 of the first bank 130a and 130b is reduced.

[0078] Accordingly, the pile-up phenomenon is induced from the top surfaces of the edges e1 and e2 of the first bank 130a and 130b to side walls of the second bank 150a and 150b.

[0079] Further, as shown in the drawings, the width w2 of the edge e2 of the second portion 130b along the short side of the pixel region P is greater than the width w1 of the edge e1 of the first portion 130a along the long side of the pixel region P.

[0080] Accordingly, regarding areas that contact the dropped organic light emitting material solution, the area at the edge e2 of the second portion 130b is wider than the area at the edge d1 of the first portion 130a.

[0081] Further, as shown in the drawings, the height h2 of the fourth portion 150b along the short side of the pixel region P is greater than the height h1 of the third portion 150a along the long side of the pixel region P.

[0082] Accordingly, in the drying process of the organic light emitting material solution, the pile-up phenomenon at the side wall of the fourth portion 150b occurs relatively more while the pile-up phenomenon at the side wall of the third portion 150a occurs relatively less. Thus, an average height of the organic light emitting layer along its short edge portions on the second bank is greater than an average height of the organic light emitting layer along the long edge portions on the second bank. Furthermore, due to the difference in pile-up phenomenon, a width of the organic light emitting layer along its short edge portions over the first bank is greater than a width of the organic light emitting layer along its long edge portions over the first bank.

[0083] Having greater pile-up phenomenon along the short edge portions beneficially acts to stretch the organic light emitting material and improves a uniformity of thickness of the display region across the pixel region P and results in the reduced pile-up along the long edge portions which forms part of the viewable display. Furthermore, the increased pile-up phenomenon along the short edge does not

significantly affect the viewable display area because the area above the driving thin film transistor DTr along the short edge is not used for display or represents a less significant portion of the display, in one embodiment.

[0084] Thus, the OLED display device of the embodiment can prevent a nonuniformity of brightness caused by reduction of a uniformity of thickness, and can improve light emission efficiency and lifetime of an organic light emitting diode.

[0085] Further, the driving thin film transistor DTr is arranged below the second portion 130b and the fourth portion 150b along the short side of the pixel region P.

[0086] In the drawings, it is shown that the driving thin film transistor DTr is arranged at the boundary portion of the lower side of the pixel region P. Alternatively, the driving thin film transistor DTr may be arranged at the boundary portion of the upper side of the pixel region P. Thus, for example, at least a portion of the driving thin film transistor DTr is under the edge of the first bank 130b along either one of the short sides in the pixel region P.

[0087] Accordingly, since the driving thin film transistor DTr is arranged below the second portion 130b and the fourth portion 150b along the short side of the pixel region P where the pile-up phenomenon occurs relatively more, an aperture ratio of the OLED display device can be improved.

[0088] Further, even though the dual banks of the first bank 130a and 130b and the second bank 150a and 150b are arranged vertically, by adjusting the widths w1 and w2 of the first and second portions 130a and 130b along the short side and the long side of the pixel region P, the aperture ratio of the OLED display device can be further improved.

[0089] In the above embodiment, it is described that, in order to improve a uniformity of thickness, the OLED display device is configured with the width w2 of the edge e2 of the second portion 130b being greater than the width w1 of the edge e1 of the first portion 130a and the height h2 of the fourth portion 150b being greater than the height h1 of the third portion 150a. In another embodiment, the OLED can be configured with either, but not necessarily both, the width w2 of the edge e2 of the second portion 130b being greater than the width w1 of the edge e1 of the first portion 130a or the height h2 of the fourth portion 150b being greater than the height h1 of the third portion 150a, and this configuration can improve a uniformity of thickness sufficiently. In other words, the widths w1 and w2 of the first bank and/or the heights h1 and h2 of the second bank can be dimensioned to improve a uniformity of thickness.

[0090] It will be apparent to those skilled in the art that various modifications and variations can be made in a display device of the present invention without departing from the spirit or scope of the disclosure. Thus, it is intended that the present invention covers the modifications and variations of this disclosure provided they come within the scope of the appended claims and their equivalents.

What is claimed is:

1. A display device comprising:

- a plurality of pixel regions each comprising an area having a short dimension and a long dimension, each of the pixel regions comprising an array of pixels to emit light;
- a plurality of boundary regions separating the pixel regions, the boundary regions comprising:
 - a first bank comprising a hydrophilic material around a perimeter of the pixel region;

a second bank comprising a hydrophobic material on a portion of the first bank around the perimeter of the pixel region, the second bank having long edge portions along the long dimension and short edge portions along the short dimension, wherein a first height of the short edge portions of the second bank is greater than a second height of the long edge portions of the second bank.

2. The display device of claim 1, wherein the first height is in a range of approximately 1.25 to 2 times the second height.

3. The display device of claim 1, wherein the first height is in a range of about 3 μm or less and is at least 0.5 μm greater than the second height.

4. The display device of claim 1, wherein the first bank comprises an inorganic material and wherein the second bank comprises an organic material.

5. The display device of claim 1, further comprising an organic light emitting layer on the first bank in the pixel

region and on at least a portion of the second bank in the boundary region, wherein the first height and the second height are dimensioned such that an average height of short edge portions of the organic light emitting layer on the second bank is greater than an average height of long edge portions of the organic light emitting layer on the second bank.

6. The display device of claim 1, further comprising an organic light emitting layer on the first bank in the pixel region and on at least a portion of the second bank in the boundary region, wherein a width of short edge portions of the first bank along the short dimension and a width of long edge portions of the first bank along the long dimension are dimensioned such that an average height of short edge portions of the organic light emitting layer on the second bank is greater than an average height of long edge portions of the organic light emitting layer on the second bank.

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专利名称(译)	有机发光二极管显示装置		
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摘要(译)

公开了一种有机发光二极管 (OLED) 显示装置，包括：基板，包括像素区域和像素区域外部的边界区域。像素区域包括具有短边和长边的区域。像素区域包括发射光的像素阵列。 OLED显示装置包括位于像素区域中和边界区域中的基板。OLED显示装置还包括位于基板上方的像素区域中的发光装置的第一电极，第一堤岸覆盖像素中的第一电极的边缘边界区域中的基板上的区域，其中沿着像素区域的短边的第一堤岸的边缘的宽度不同于沿着像素区域的长边的第一堤岸的边缘的宽度，并且第二个银行位于边界地区第一个银行的一部分。

